

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Steven J. Koester

Examiner: Anh D. Mai

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For: SEMICONDUCTOR FIELD-EFFECT
TRANSISTOR HAVING STRAINED-LAYER
(Previously Amended)

Dated: December 26, 2006

Confirmation No: 7401

Mail Stop Amendment
Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated September 25, 2006, applicants respectfully request the Examiner to reconsider the application in view of the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on December 26, 2006.

Dated: December 26, 2006



Steven Fischman